IN THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the application.

Listing of Claims

1.   (Currently amended) A method for reporting bit line or driver failures, comprising:

  detecting a predetermined number of consecutive correctable errors;

  storing a description for each of the predetermined number of correctable errors;

  determining whether the descriptions for the predetermined number of correctable errors are the same; and

  reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same.

2.   (Original) The method of claim 1, wherein the step of detecting a predetermined number of correctable errors comprises performing a periodic scan for a processor.

3.   (Original) The method of claim 1, wherein the step of storing a description for each of the predetermined number of correctable errors comprises storing the descriptions in an error data structure.

4.   (Original) The method of claim 3, wherein the error data structure comprises an error table.

5.   (Currently amended) ~~The method of claim 3, further~~ A method for reporting failures, comprising:

  detecting a predetermined number of consecutive correctable errors;

  storing a description for each of the predetermined number of correctable

errors in an error data structure;

determining whether the descriptions for the predetermined number of correctable errors are the same;

reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same; and

clearing the error data structure if a correctable error is not encountered on an event scan call before detecting the predetermined number of consecutive correctable errors.

6.   (Original) The method of claim 1, wherein the step of reporting a bit line or driver failure comprises:

creating an error log; and

returning the error log to an operating system.

7.   (Original) The method of claim 1, wherein the predetermined number is five.

8.   (Currently amended) ~~The method of claim 1~~ A method for reporting failures, comprising:

detecting a predetermined number of consecutive correctable errors;

storing a description for each of the predetermined number of correctable errors,wherein each description comprises an address at which an error occurred and an error signature that indicates which bit is bad;

determining whether the descriptions for the predetermined number of correctable errors are the same; and

reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same.

9.   (Currently amended) ~~The method of claim 1, further~~ A method for reporting failures, comprising:

detecting a predetermined number of consecutive correctable errors;

storing a description for each of the predetermined number of correctable

errors;

determining whether the descriptions for the predetermined number of correctable errors are the same;

reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same; and

deconfiguring [[the]] a processor if the descriptions for the predetermined number of errors are the same.

10.   (Original) The method of claim 9, wherein the step of deconfiguring the processor comprises dynamically deconfiguring the processor.

11.   (Original) The method of claim 9, wherein the step of deconfiguring the processor comprises deconfiguring the processor at boot time.

12.   (Currently amended) The method of claim 1, further A method for reporting failures, comprising:

detecting a predetermined number of consecutive correctable errors;

storing a description for each of the predetermined number of correctable errors;

determining whether the descriptions for the predetermined number of correctable errors are the same;

reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same; and

replacing [[the]] a processor if the descriptions for the predetermined number of correctable errors are the same.

13.   (Currently amended) An apparatus for reporting bit line or driver failures, comprising:

detection means for detecting a predetermined number of consecutive correctable errors;

storage means for storing a description for each of the predetermined number of correctable errors;

determination means for determining whether the descriptions for the predetermined number of correctable errors are the same; and

reporting means for reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same.

14.   (Original) The apparatus of claim 13, wherein the detection means comprises performing a periodic scan for a processor.

15.   (Original) The apparatus of claim 13, wherein the storage means comprises an error data structure.

16.   (Original) The apparatus of claim 15, wherein the error data structure comprises an error table.

17.   (Currently amended) ~~The apparatus of claim 15, further~~ An apparatus for reporting failures, comprising:

detection means for detecting a predetermined number of consecutive correctable errors;

an error data structure for storing a description for each of the predetermined number of correctable errors;

determination means for determining whether the descriptions for the predetermined number of correctable errors are the same;

reporting means for reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same; and

means for clearing the error data structure if a correctable error is not encountered on an event scan call before detecting the predetermined number of consecutive correctable errors.

18. (Original) The apparatus of claim 13, wherein the reporting means comprises:

means for creating an error log; and

means for returning the error log to an operating system.

19. (Original) The apparatus of claim 13, wherein the predetermined number is five.

20. (Currently amended) ~~The apparatus of claim 13~~ An apparatus for reporting failures; comprising:

detection means for detecting a predetermined number of consecutive correctable errors;

storage means for storing a description for each of the predetermined number of correctable errors, wherein each description comprises an address at which an error occurred and an error signature that indicates which bit is bad;

determination means for determining whether the descriptions for the predetermined number of correctable errors are the same; and

reporting means for reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same.

21. (Currently amended) ~~The apparatus of claim 13, further~~ An apparatus for reporting failures, comprising:

detection means for detecting a predetermined number of consecutive correctable errors;

storage means for storing a description for each of the predetermined number of correctable errors;

determination means for determining whether the descriptions for the predetermined number of correctable errors are the same;

reporting means for reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same; and

deconfiguration means for deconfiguring [[the]] a processor if the descriptions for the predetermined number of errors are the same.

22. (Original) The apparatus of claim 21, wherein the deconfiguration means comprises means for dynamically deconfiguring the processor.

23. (Original) The apparatus of claim 21, wherein the deconfiguration means comprises means for deconfiguring the processor at boot time.

24. (Currently amended) ~~The apparatus of claim 13, further~~ An apparatus for reporting failures, comprising:

detection means for detecting a predetermined number of consecutive correctable errors;

storage means for storing a description for each of the predetermined number of correctable errors;

determination means for determining whether the descriptions for the predetermined number of correctable errors are the same;

reporting means for reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same; and

means for replacing [[the]] a processor if the descriptions for the predetermined number of correctable errors are the same.

25. (Currently amended) An apparatus for reporting bit line or driver failures, comprising:

a processor; and

a memory, coupled to the processor, having stored therein an error data structure,

wherein the processor detects a predetermined number of consecutive correctable errors, stores a description for each of the predetermined number of correctable errors in the error data structure, determines whether the descriptions for the predetermined number of correctable errors are the same, and reports a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same.

26. (Original) The apparatus of claim 25, wherein the processor detects a predetermined number of consecutive correctable errors by performing a periodic scan for the processor.

27. (Original) The apparatus of claim 25, wherein the error data structure comprises an error table.

28. (Original) The apparatus of claim 25, wherein the processor reports a bit line or driver failure by creating an error log, and returning the error log to an operating system.

29. (Original) The apparatus of claim 25, wherein the predetermined number is five.

30. (Currently amended) ~~The apparatus of claim 25~~ An apparatus for reporting failures, comprising:

a processor; and

a memory, coupled to the processor, having stored therein an error data structure, wherein the processor detects a predetermined number of consecutive correctable errors, stores a description for each of the predetermined number of correctable errors in the error data structure, determines whether the descriptions for the predetermined number of correctable errors are the same, and reports a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same, wherein each description comprises an address at which an error occurred and an error signature that indicates which bit is bad.

31. (Currently amended) A computer program product, in a computer readable medium, for reporting bit line or driver failures, comprising:

instructions for detecting a predetermined number of consecutive correctable errors;

instructions for storing a description for each of the predetermined number of correctable errors;

instructions for determining whether the descriptions for the predetermined number of correctable errors are the same; and

instructions for reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same.